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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/904,622	07/13/2001	Marco Michael Rengan	RPS920010005US1	2989

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EXAMINER

NGUYEN, KIMNHUNG T

ART UNIT	PAPER NUMBER
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2677

DATE MAILED: 11/15/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

DETAILED ACTION

1. In view of the Appeal Brief filed on 8/18/05, PROSECUTION IS HEREBY REOPENED. New grounds of rejection set forth below.

To avoid abandonment of the application, appellant must exercise one of the following two options:

(1) file a reply under 37 CFR 1.111 (if this Office action is non-final) or a reply under 37 CFR 1.113 (if this Office action is final); or,

(2) initiate a new appeal by filing a notice of appeal under 37 CFR 41.31 followed by an appeal brief under 37 CFR 41.37. The previously paid notice of appeal fee and appeal brief fee can be applied to the new appeal. If, however, the appeal fees set forth in 37 CFR 41.20 have been increased since they were previously paid, then appellant must pay the difference between the increased fees and the amount previously paid.

2. This Application has been examined. The claims 1-7 and 24-37 are pending. The examination results are as following.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

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4. Claims 1, 2, 4, 6, 7, 24-25, 27, 29-32, 34, 36 and 37 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chee (US 5,694,141) in view of Ranganathan (US 5,764,201).

Regarding claims 1, 24, 31, Chee discloses in figure 15, a method for providing displaying control on a computer system having a first display device (LCD 14) and a second display device (CRT 24), the method comprising allocating a first memory location (56) for storing contents to be displayed by said first display device, wherein said first memory location is accessible by a video display controller (122), allocating a second memory location (56') for storing contents to be displayed by said second display device, wherein the second memory location is accessible by said video display controller; in response to a selection of a split display mode, retaining information in the first memory location and updating information in said second memory location, such that contents displayed on said first display device are different from contents displayed on the second display device (see col. 17, lines 45-54).

However, Chee does not disclose that a concurrent display mode, providing identical information, such that contents displayed on the first display device are identical to contents displayed on the second display device.

Ranganathan discloses in fig. 10A, a concurrent display mode, providing identical information, such that contents displayed on the first display (CRT 24) device are identical to contents displayed on the second display device (LCD 22, see col. 10, lines 66-67, col. 11, lines 1-1-2).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement the using of the identical information, such that contents displayed on the first display device are identical to contents displayed on the second display device as taught

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by Rangathan into the system of Chee having a first and second memories because this would provide to the user the identical images are displayed on the external CRT 24 and LCD panel 22 (see col. 11, lines 1-2).

Regarding claims 2, 25, 32, Chee providing information from a frame buffer (38, fig. 5) to the first and second memory locations (56, 56'). However, Chee does not disclose the identification information. Ranganathan discloses the identification in formation of fig. 10A as discussed above.

Regarding claims 4, 27, 34, Chee discloses setting a pointer pointing from a frame buffer (66) to said first and second memory locations (56, 56'). However, Chee does not disclose the providing identical information. Ranganathan discloses the identification in formation of fig. 10A as discussed above.

Regarding claims 6, 29, 36, Chee discloses the first display device (CRT) is external from said computer system and the second display device (LCD) is internal to said commuter system (see fig. 5)

Regarding claims 7, 30, 37, Chee discloses the selection between the concurrent and split display mode are made via a soft key function (see central processing unit CPU with input device and may run a program see col. 6, lines 26-33).

5. Claims 3, 5, 26, 28, 33 and 35 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chee (US 5,694,141) in view of Ranganathan (US 5,764,201) and in view of Komeichi (US 5,929,871).

Chee and Rangathan disclose every feature of the claimed invention, excluding the updating information further includes allocating a second frame buffer; and providing information from the second frame buffer to the second memory location while providing information from the frame buffer to the first memory location; or the updating information further includes allocating second frame buffer and setting a second pointer pointing from said second frame buffer to the first memory location.

Komeichi discloses in figures 4-5 a second frame buffer (39); and providing information from the second frame buffer to the second memory (see second store region 39-2) location while providing information from the frame buffer (38) to said first memory location (see first store region 39-1); or the updating information further includes allocating second frame buffer (39) and setting a second pointer pointing from said second frame buffer (39-3) to said first memory location (see first store region 39-1) (see column 3, lines 35-49).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to implement a second frame buffer; and providing information from said second frame buffer to said second memory location while providing information from said frame buffer to said first memory location as taught by Komeichi into the system having the first and second

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display of Chee and Ranganathan because this would improve the utilization efficiency of the memory capacity provided by memories forming the frame buffer part relatively simple circuit (see col. 4, lines 43-46).

Correspondence

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimnhung Nguyen whose telephone number is (571) 272-7698.

The examiner can normally be reached on MON-FRI, FROM 8:30 AM-5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amr Awad can be reached on (571) 272-7764. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Kimnhung Nguyen
November 7, 2005

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